

# ***B1\_EVB\_P1\_V10***

<b>Revision</b>	<b>Date</b>	<b>Change Description</b>
V1.0	20241111	Initial version

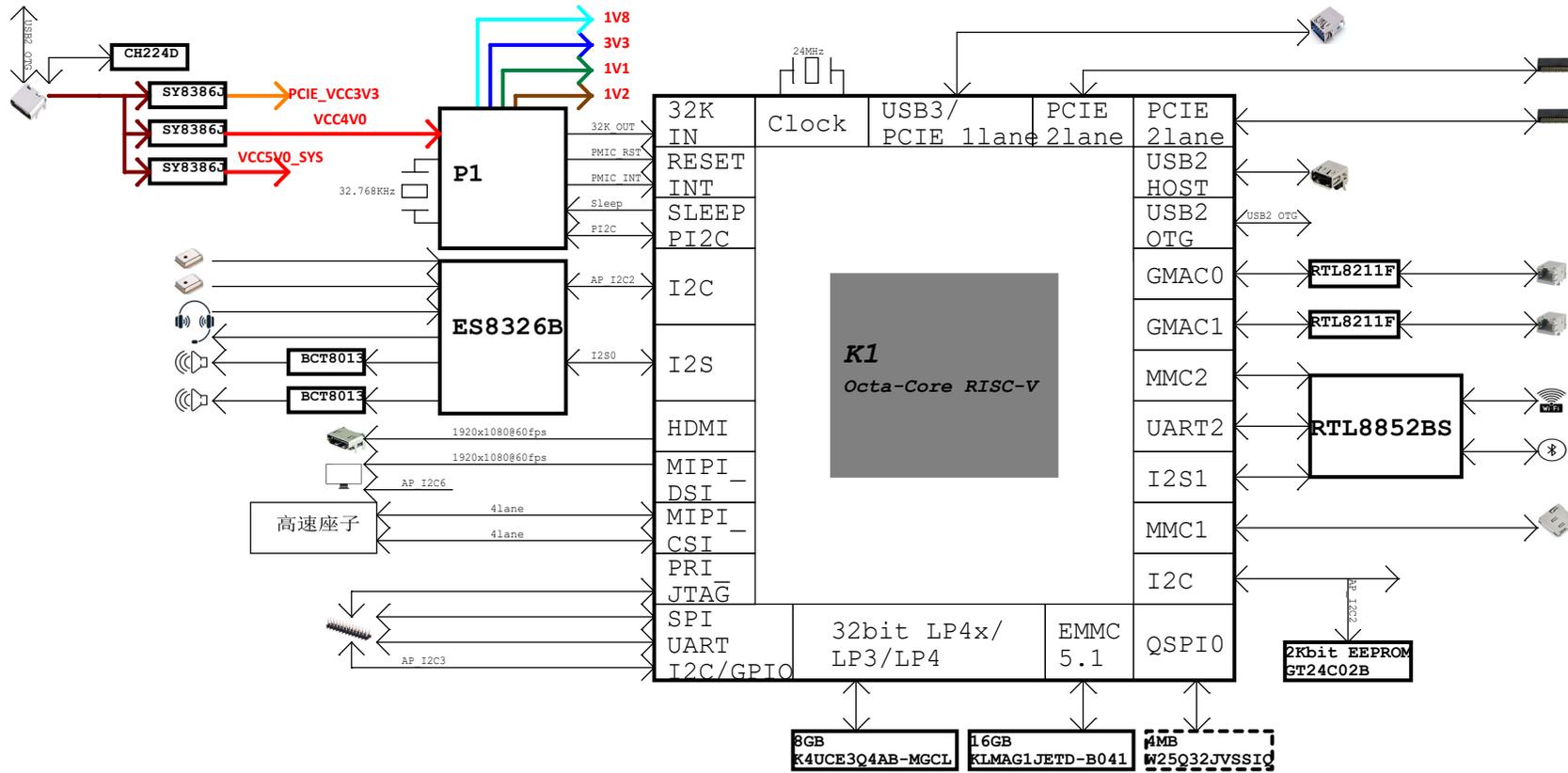
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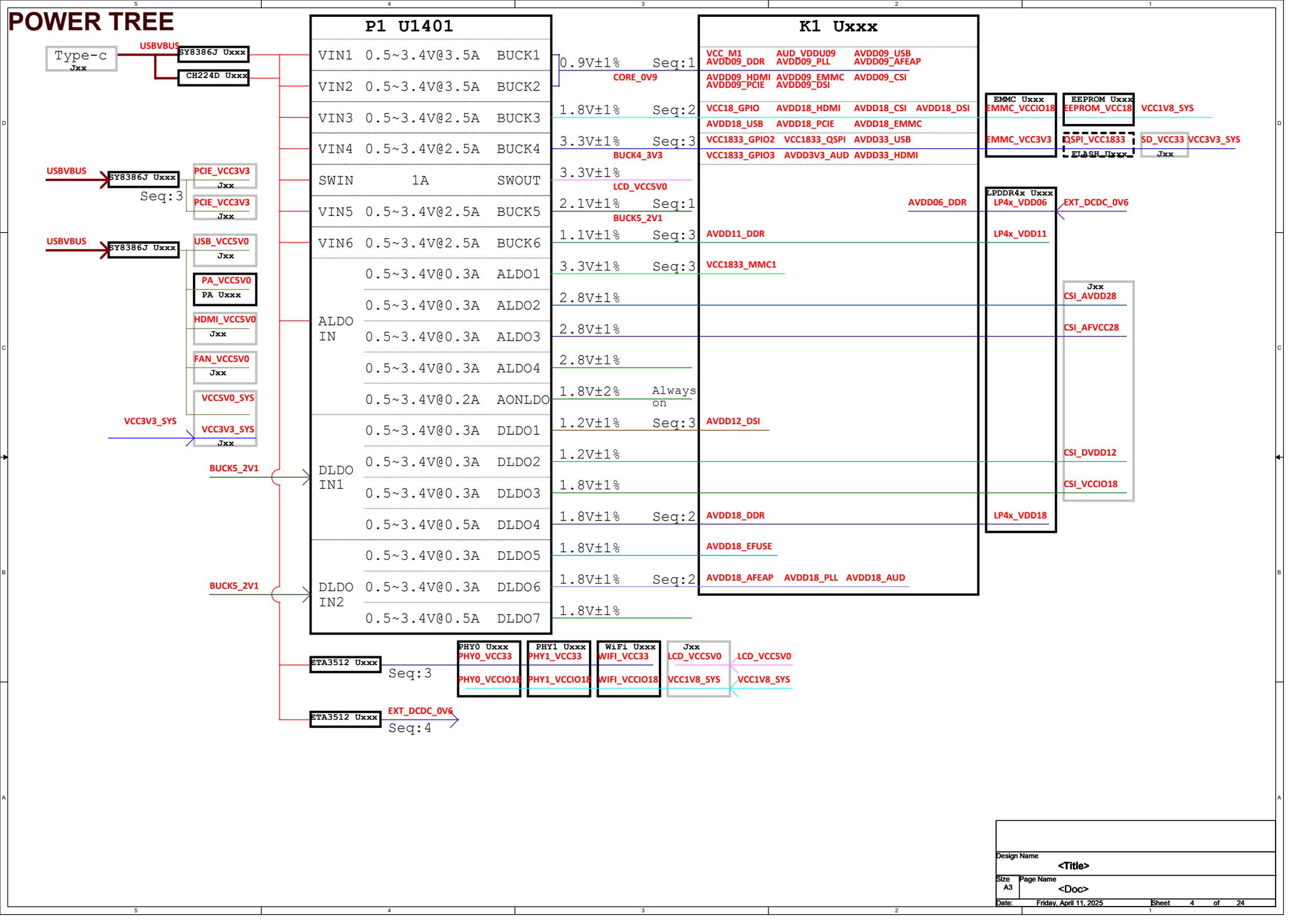
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# BLOCK

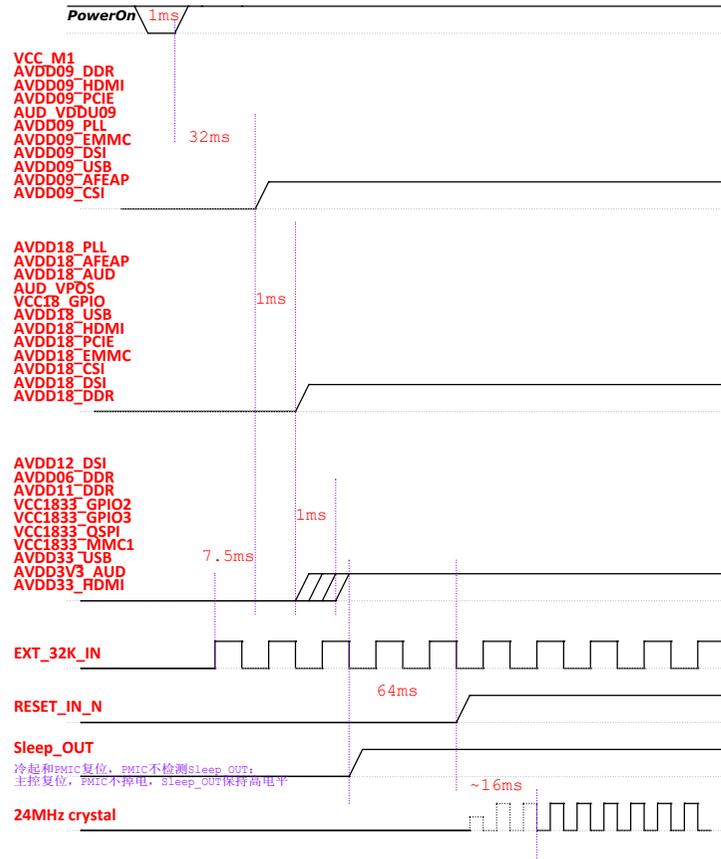




# POWER SEQUENCE

Control by PMIC

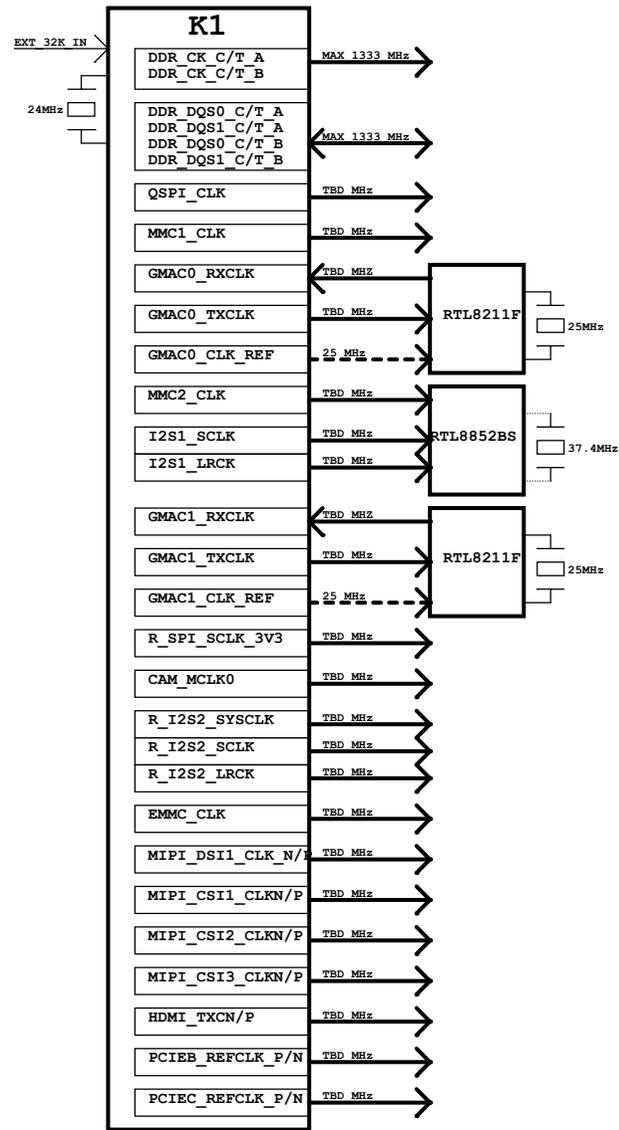
冷起自动上电，后续长按关机，短接开机



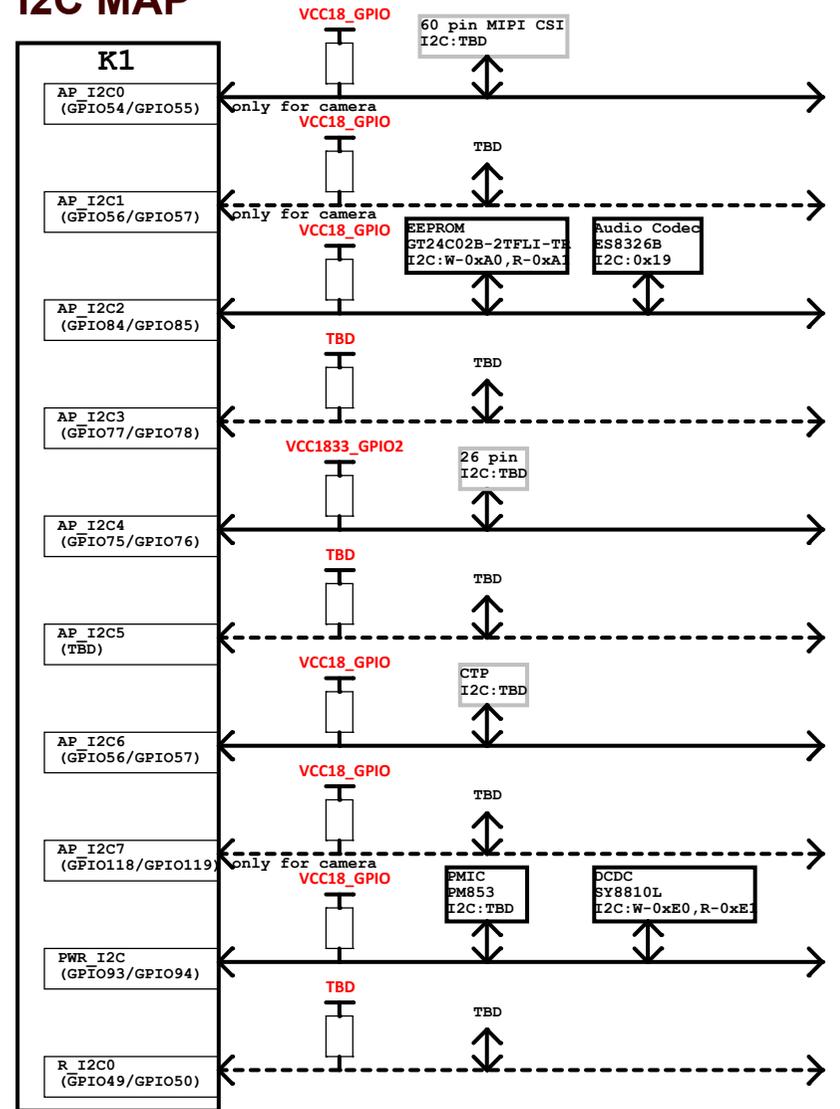
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# CLOCK MAP

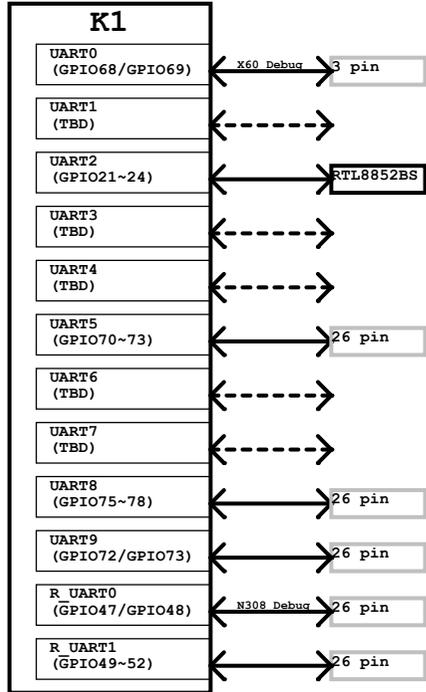


# I2C MAP

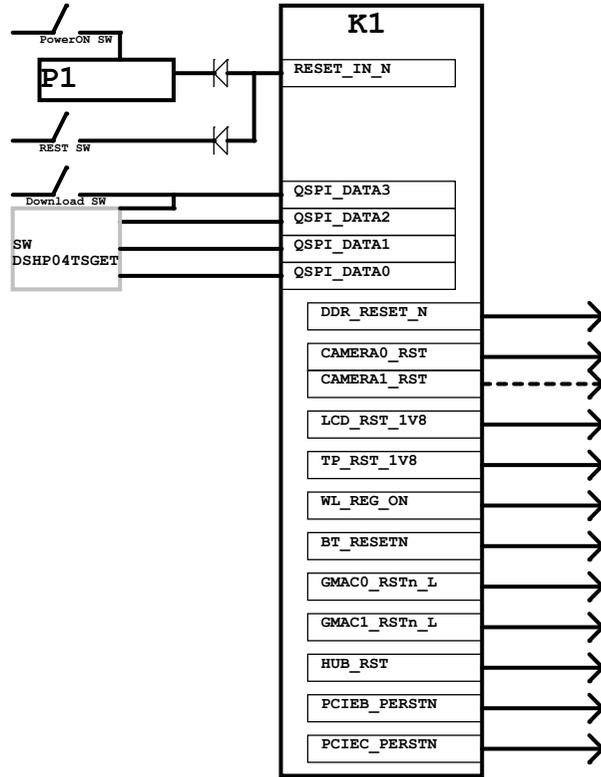


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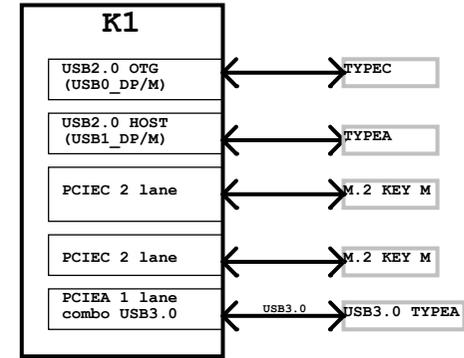
# UART MAP



# SW&RESET MAP



# PCIE/USB MAP



# GPIO ASSIGNMENT

PIN	Define	CFG	Function
GPIO0	GMAC0_RXDV	1	Ethernet -GMAC0
GPIO1	GMAC0_RXD0	1	
GPIO2	GMAC0_RXD1	1	
GPIO3	GMAC0_RXCLK	1	
GPIO4	GMAC0_RXD2	1	
GPIO5	GMAC0_RXD3	1	
GPIO6	GMAC0_TXD0	1	
GPIO7	GMAC0_TXD1	1	
GPIO8	GMAC0_TXCLK	1	
GPIO9	GMAC0_TXD2	1	
GPIO10	GMAC0_TXD3	1	
GPIO11	GMAC0_TXEN	1	
GPIO12	GMAC0_MDC	1	
GPIO13	GMAC0_MDIO	1	
GPIO14	GMAC0_INT_N	1	
GPIO15	MMC2_DATA3	1	WiFi/BT
GPIO16	MMC2_DATA2	1	
GPIO17	MMC2_DATA1	1	
GPIO18	MMC2_DATA0	1	
GPIO19	MMC2_CMD	1	
GPIO20	MMC2_CLK	1	
GPIO21	UART2_TXD	1	
GPIO22	UART2_RXD	1	
GPIO23	UART2_CTS_N	1	
GPIO24	UART2_RTS_N	1	
GPIO25	I2S1_SCLK	1	Ethernet -GMAC1
GPIO26	I2S1_LRCK	1	
GPIO27	I2S1_TXD	1	
GPIO28	I2S1_RXD	1	
GPIO29	GMAC1_RXDV	1	
GPIO30	GMAC1_RXD0	1	
GPIO31	GMAC1_RXD1	1	
GPIO32	GMAC1_RXCLK	1	
GPIO33	GMAC1_RXD2	1	
GPIO34	GMAC1_RXD3	1	
GPIO35	GMAC1_TXD0	1	
GPIO36	GMAC1_TXD1	1	
GPIO37	GMAC1_TXCLK	1	
GPIO38	GMAC1_TXD2	1	
GPIO39	GMAC1_TXD3	1	
GPIO40	GMAC1_TXEN	1	
GPIO41	GMAC1_MDC	1	
GPIO42	GMAC1_MDIO	1	
GPIO43	GMAC1_INT_N	1	
GPIO44	LCD_BL_PWM_1V8	4	LCD/CTP
GPIO45	GMAC0_CLK_REF	1	GMAC0
GPIO46	GMAC1_CLK_REF	1	GMAC1

PIN	Define	CFG	Function
GPIO110	GMAC0_RSTn_L	0	GMAC0
GPIO115	GMAC1_RSTn_L	0	GMAC1
GPIO116	WL_DIS_N	0	WiFi/BT
GPIO117	PCIEC_CLKREQN	4	PCIEC
GPIO118	I2S0_SCLK	3	Audio Codec
GPIO119	I2S0_LRCK	3	
GPIO120	I2S0_OUT	3	
GPIO121	I2S0_IN	3	
GPIO122	I2S0_SYSCLK	3	USB3_HUB
GPIO123	HUB_PWREN	0	
GPIO124	HUB_RST	0	USB2
GPIO125	VBUS_ON0	1	Audio Codec
GPIO126	CODEC_IRQ	0	
GPIO127	PA_SHUTDOWN	0	

PIN	Define	CFG	Function
GPIO53	CAM_MCLK0	1	CAMERA0
GPIO54	CAM_I2C0_SCL	1	
GPIO55	CAM_I2C0_SDA	1	
GPIO56	AP_I2C6_SCL	5	LCD/CTP
GPIO57	AP_I2C6_SDA	5	
GPIO58	TP_INT_1V8	0	CAMERA0
GPIO111	CAMERA0_RST	1	
GPIO112	PCIEC_WAKEN	3	PCIEC
GPIO113	CAMERA0_PDN	1	CAMERA0
GPIO114	TP_RST_1V8	0	LCD/CTP
GPIO63	BT_RESETN	0	WiFi/BT
GPIO64	AP_WAKE_BT	0	
GPIO65	BT_WAKE_AP	0	
GPIO66	WL_WAKE_AP	0	
GPIO67	WL_REG_ON	0	X60 Debug
GPIO68	UART0_TXD	2	
GPIO69	UART0_RXD	2	

PIN	Define	CFG	Function
GPIO59	PCIEB_PERSTN	4	PCIEB
GPIO60	PCIEB_WAKEN	4	
GPIO61	PCIEB_CLKREQN	4	
GPIO62	PCIEC_PERSTN	4	PCIEC
GPIO70	GPIO70	1	26 pin
GPIO71	GPIO71	1	
GPIO72	GPIO72	1	
GPIO73	GPIO73	1	
GPIO74	GPIO74	0	

红色字体GPIO表示默认、持续上拉，等效上拉电阻约60K。需要软件修改才能解除默认上拉状态

PIN	Define	CFG	Function
GPIO93	PI2C_SCL	0	PMIC
GPIO94	PI2C_SDA	0	
GPIO95	SLEEP_OUT	0	LED
GPIO96	GPIO96	1	
GPIO97	USB3_PWREN	1	USB3_HUB
GPIO81	LCD_RST_1V8	0	LCD/CTP
GPIO82	LCD_BL_EN_1V8	0	
GPIO83	LCD_PWR_EN_1V8	0	EEPROM Audio
GPIO84	AP_I2C2_SCL	4	
GPIO85	AP_I2C2_SDA	4	
GPIO86	HDMI_SCL	1	HDMI_OUT
GPIO87	HDMI_SDA	1	
GPIO88	HDMI_CEC	1	
GPIO89	HDMI_HPD	1	26 pin
GPIO90	GPIO90	0	
GPIO91	GPIO91	0	
GPIO92	GPIO92	0	

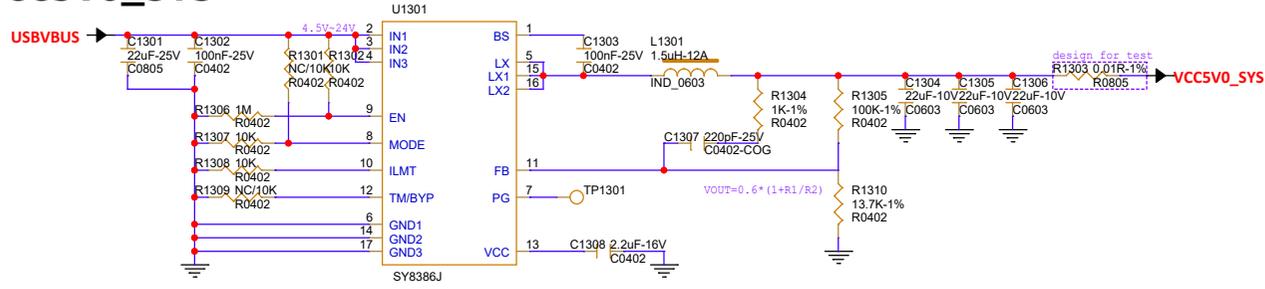
PIN	Define	CFG	Function
GPIO98	QSPI_DATA3	0	SPI FLASH
GPIO99	QSPI_DATA2	0	
GPIO100	QSPI_DATA1	0	
GPIO101	QSPI_DATA0	0	
GPIO102	QSPI_CLK	0	
GPIO103	QSPI_CS1	0	

PIN	Define	CFG	Function
GPIO104	MMC1_DATA3	0	TF CARD
GPIO105	MMC1_DATA2	0	
GPIO106	MMC1_DATA1	0	
GPIO107	MMC1_DATA0	0	
GPIO108	MMC1_CMD	0	
GPIO109	MMC1_CLK	0	

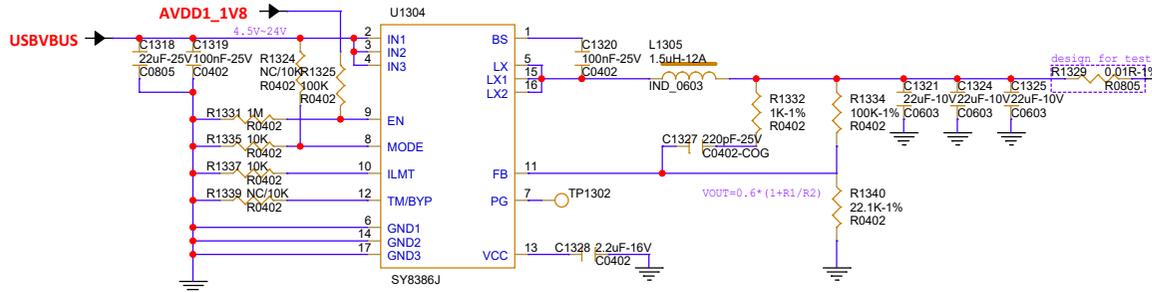
PIN	Define	CFG	Function
GPIO75	SPI3_SCLK_3V3	2	26 pin
GPIO76	SPI3_CS_3V3	2	
GPIO77	SPI3_MOSI_3V3	2	
GPIO78	SPI3_MISO_3V3	2	
GPIO79	FAN_PWM	2	FAN
GPIO80	SD_CD_3V3	1	TF CARD

PIN	Define	CFG	Function
GPIO47	R_UART0_TXD_3V3	1	26 pin
GPIO48	R_UART0_RXD_3V3	1	
GPIO49	GPIO_49_3V3	0	
GPIO50	GPIO_50_3V3	0	
GPIO51	AP_I2C4_SCL_3V3	4	
GPIO52	AP_I2C4_SDA_3V3	4	

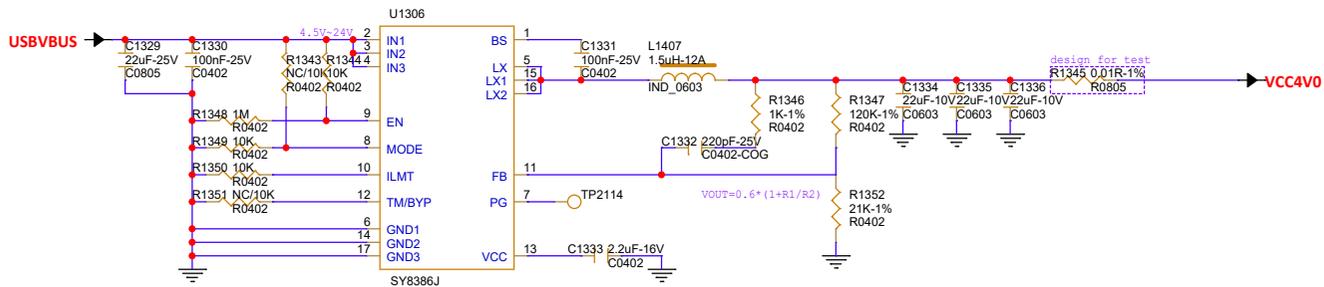
# VCC5V0\_SYS



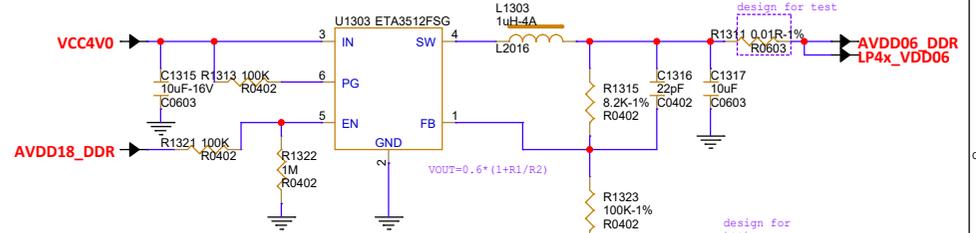
# PCIE\_VCC3V3



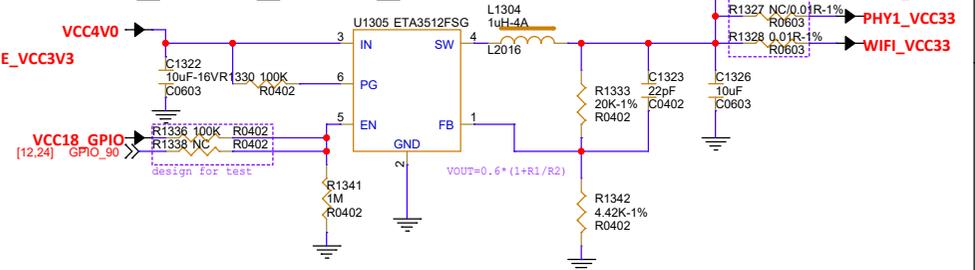
# PMIC\_VCC4V0



# EXT\_DCDC\_0V6

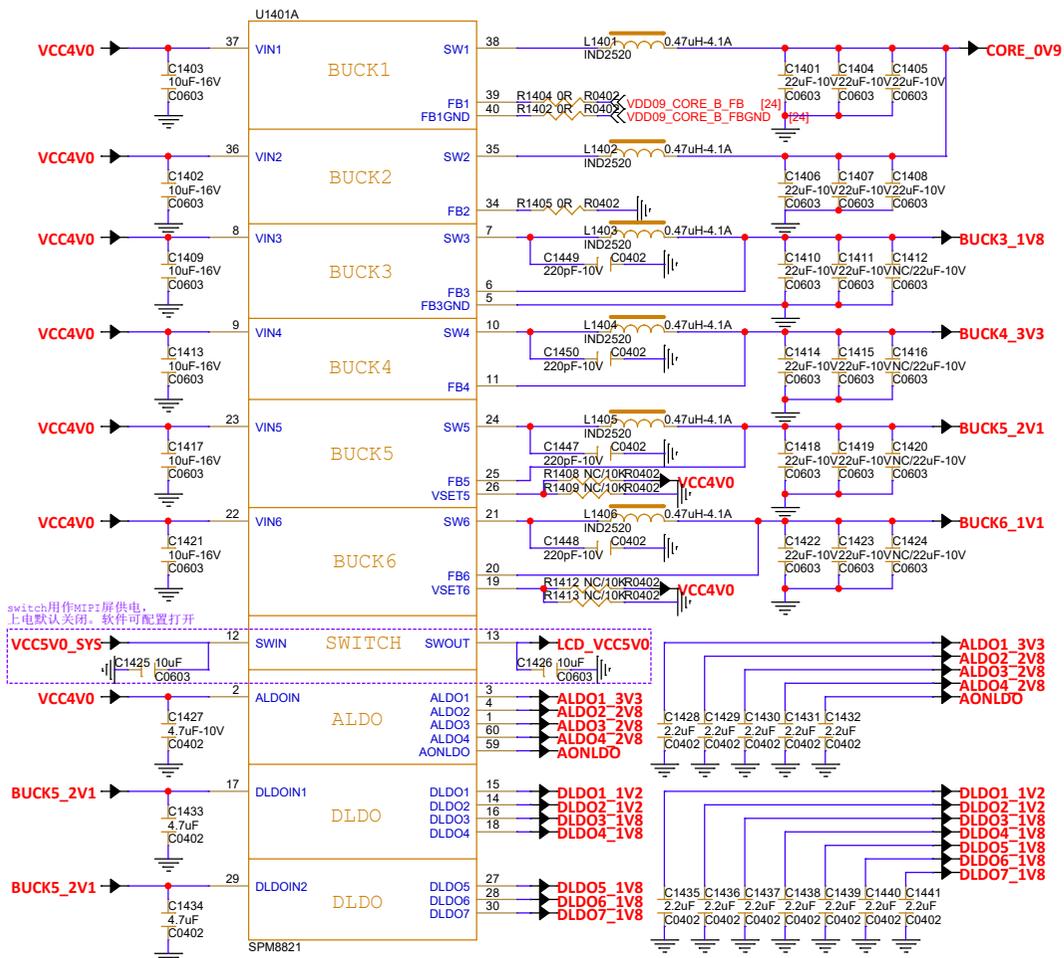


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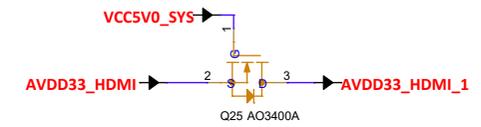
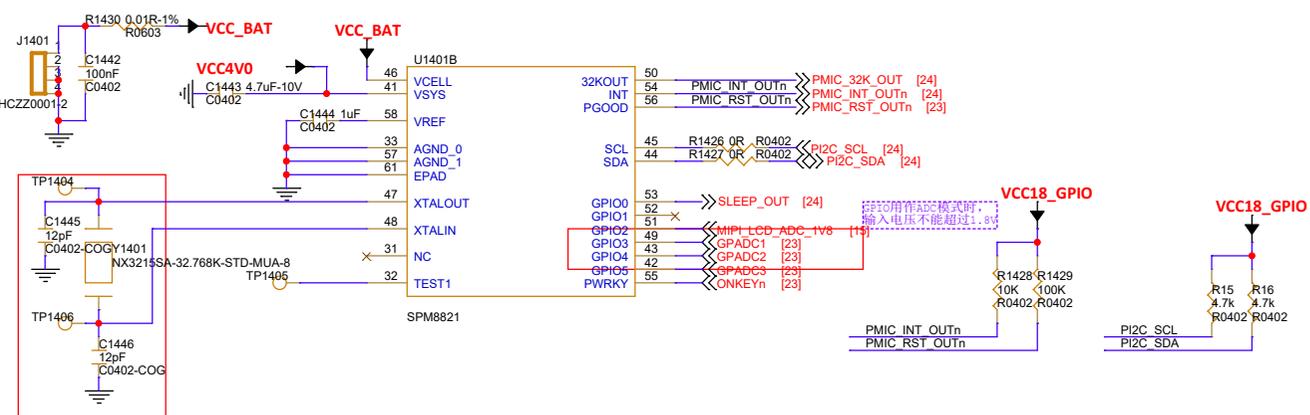
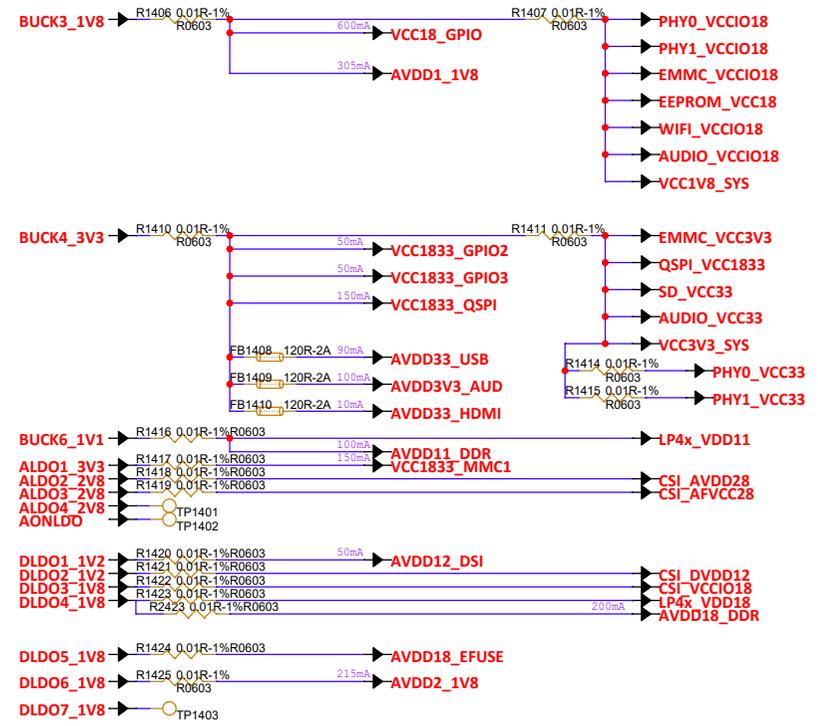
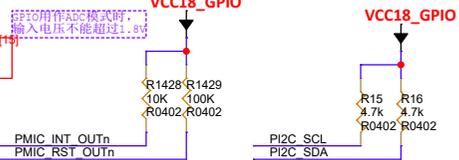


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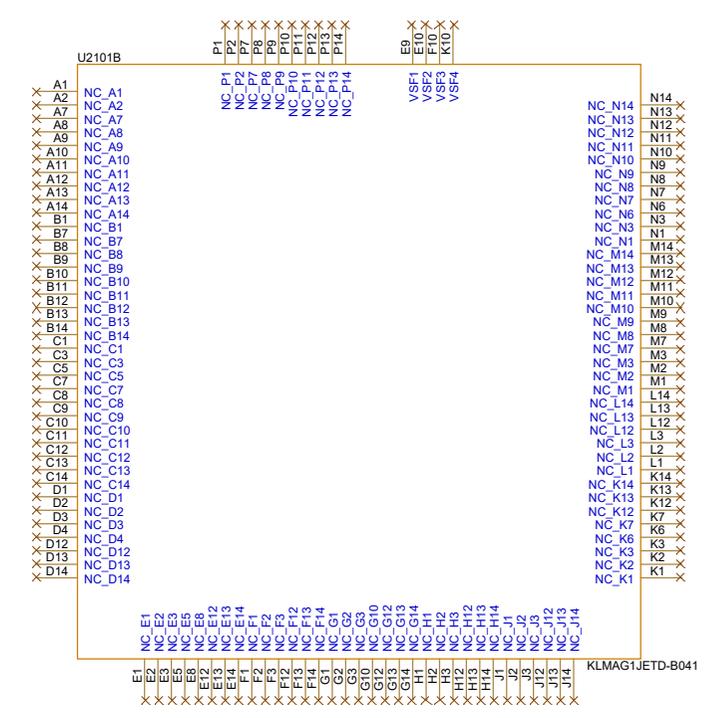
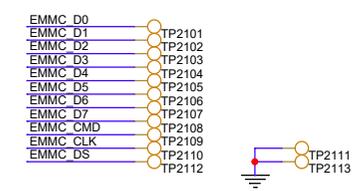
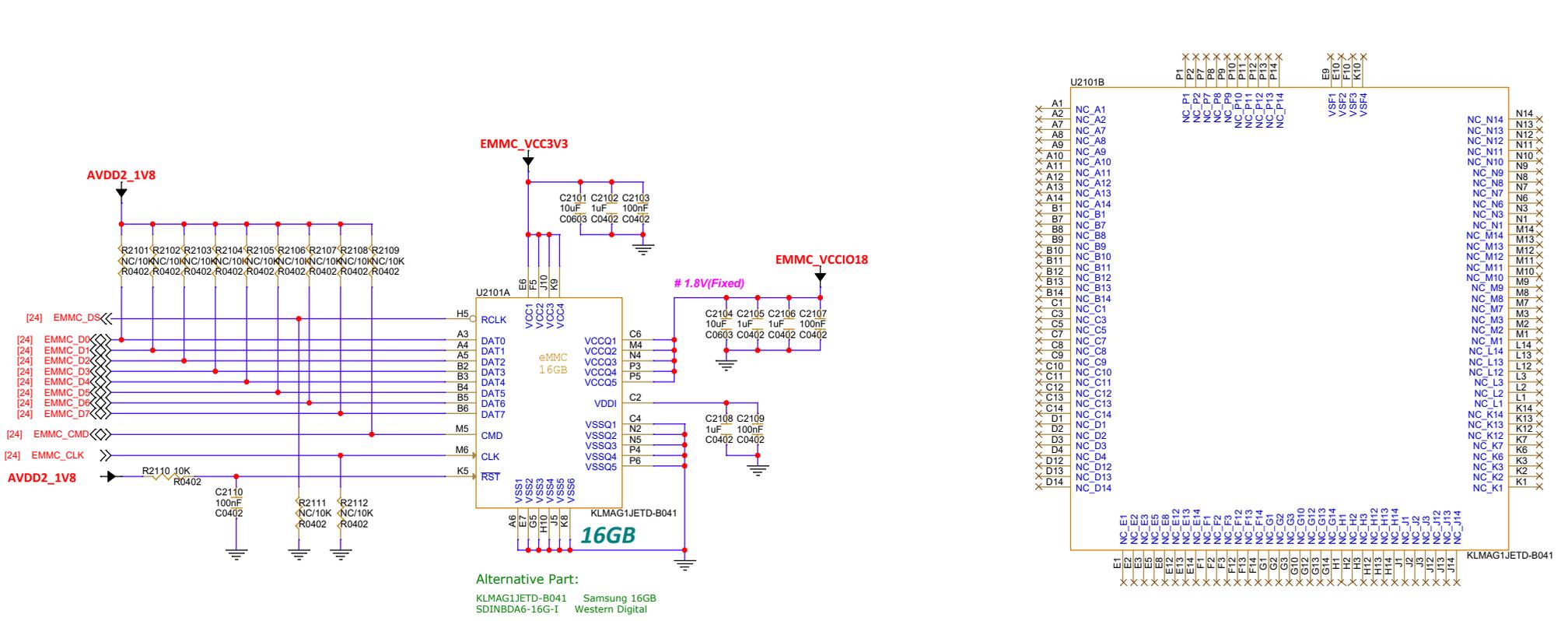


switch用作MIPI屏供电，  
上电默认关闭，软件可配置打开

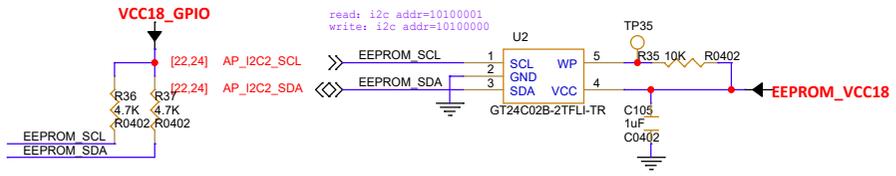


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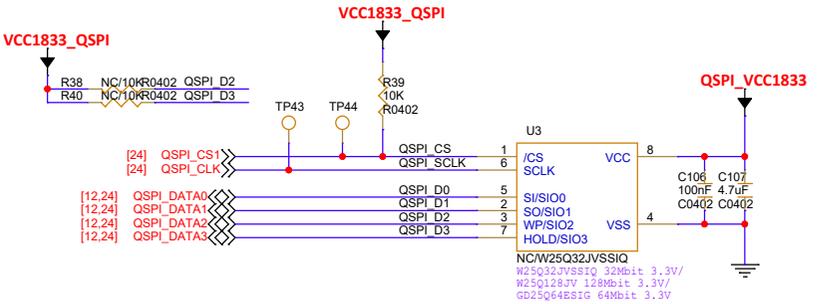
# EMMC



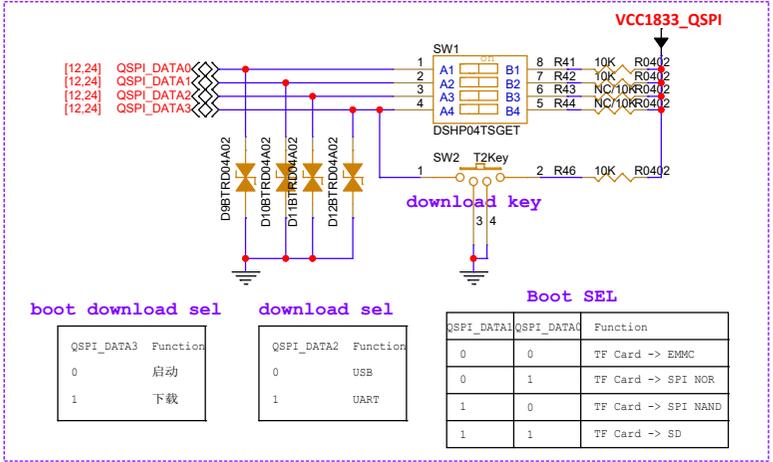
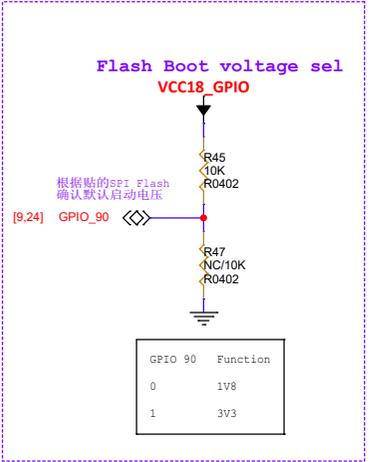
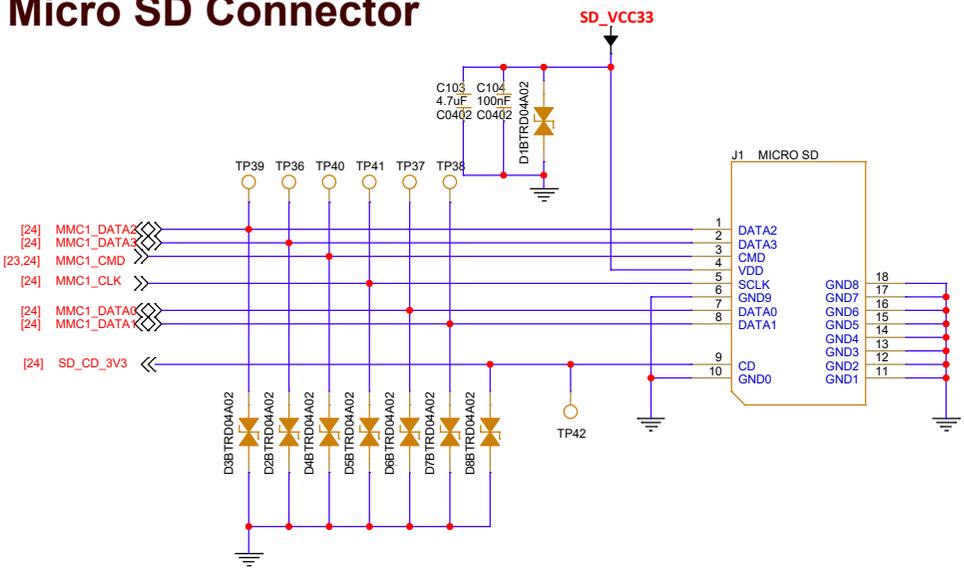
# EEPROM



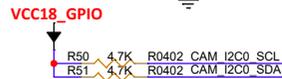
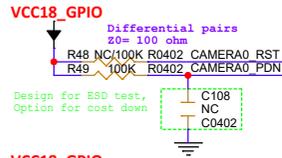
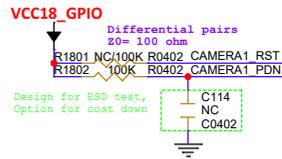
# SPI FLASH



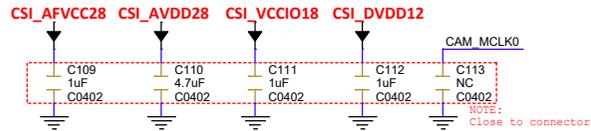
# Micro SD Connector



# CAMERA

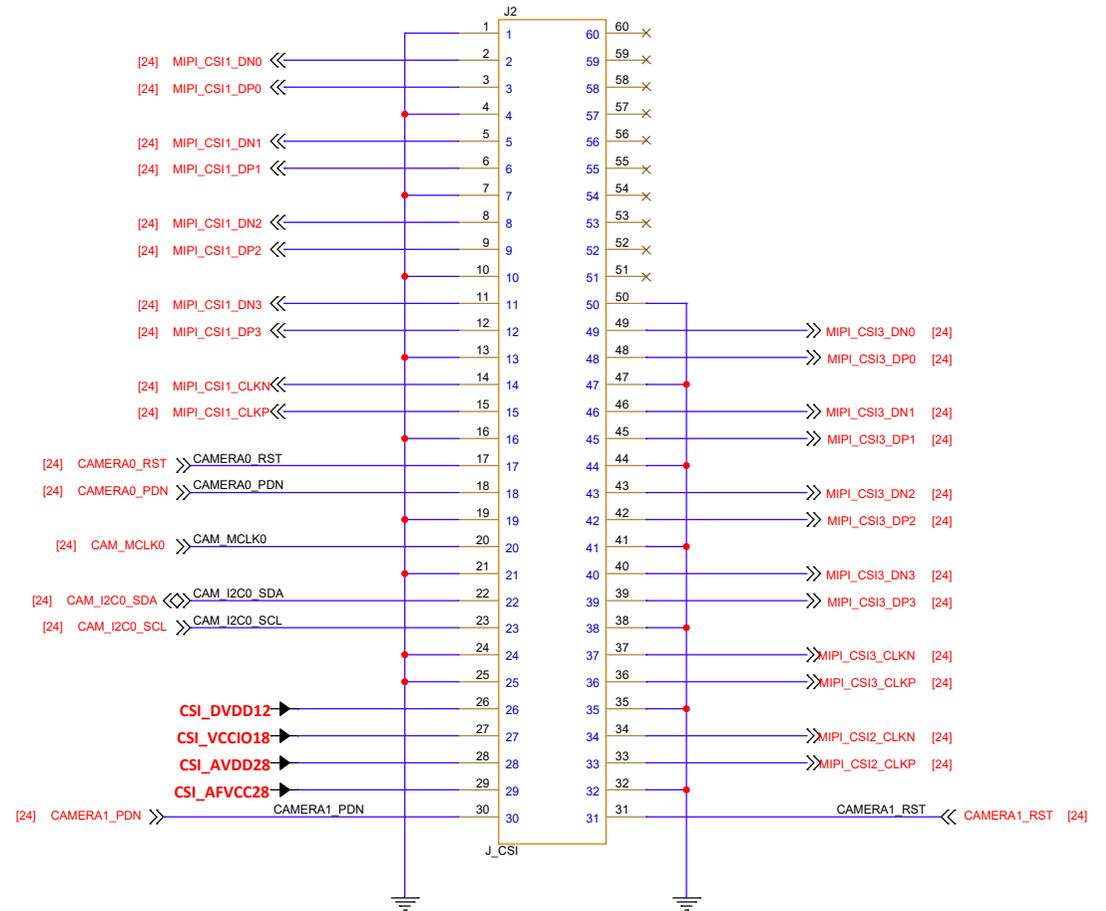


CSI1、2、3共用一组MCLK和I2C,  
CSI2、3共用一组RST、PDN;  
因此在自研SBC大尺寸中仅可单摄使用

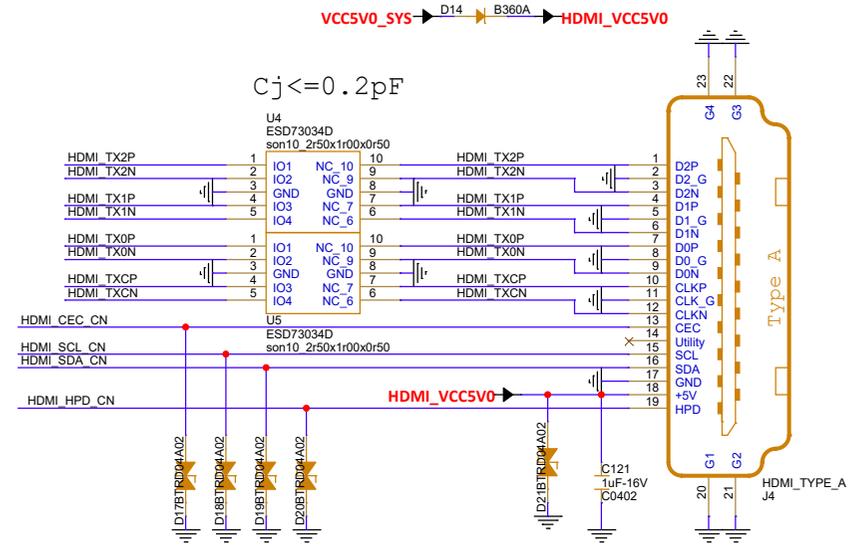
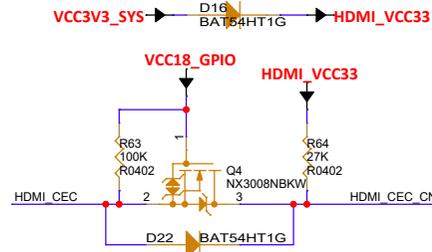
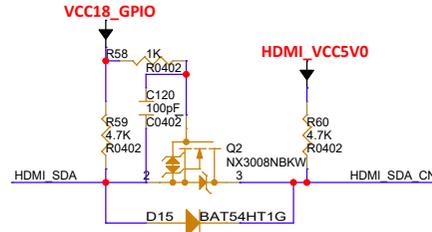
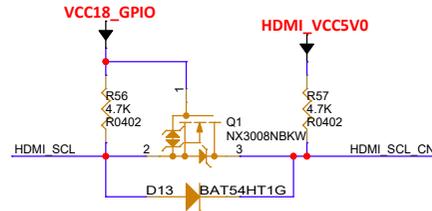
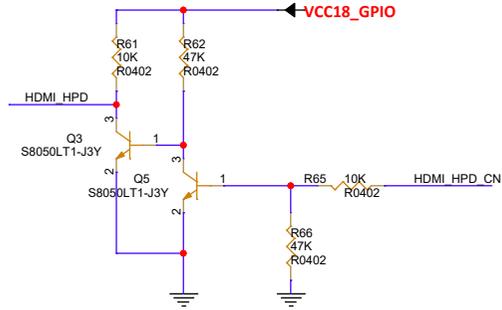
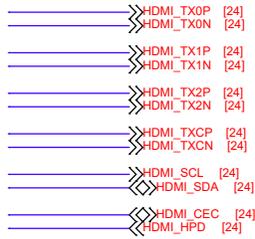


	信号	时钟	I2C	RST&PDN	
4+2+2	CSI1	MIPI_CSI1_D0	CAM_MCLK0	CAM_I2C0	CAMERA0_PDN
		MIPI_CSI1_D1			
		MIPI_CSI1_D2			
		MIPI_CSI1_D3			
		MIPI_CSI1_CLK			
CSI2	MIPI_CSI3_D2	CAM_MCLK1	CAM_I2C1	CAMERA1_PDN	
	MIPI_CSI3_D3	MIPI_CSI2_CLK		CAMERA1_RST	
CSI3	MIPI_CSI3_D0	CAM_MCLK2	CAM_I2C2	CAMERA2_PDN	
	MIPI_CSI3_D1	MIPI_CSI3_CLK		CAMERA2_RST	

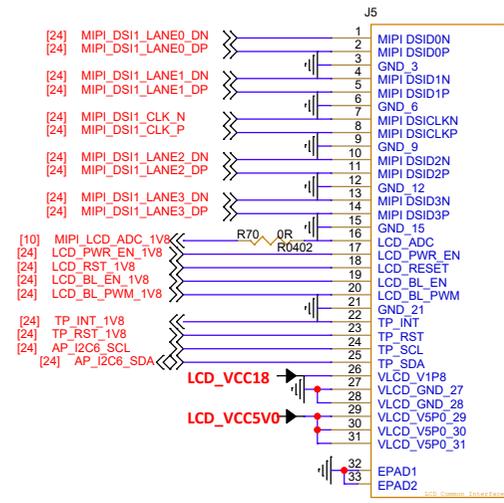
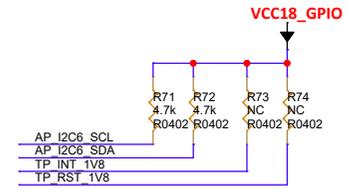
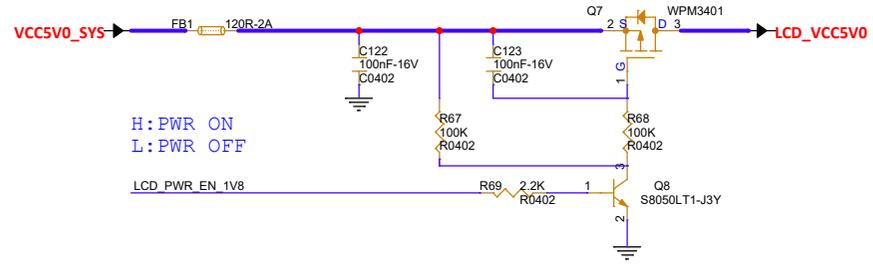
	信号线		I2C	RST&PDN		
4+4	CSI1	MIPI_CSI1_D0	CAM_MCLK0	CAM_I2C0	CAMERA0_PDN	
		MIPI_CSI1_D1				
		MIPI_CSI1_D2				
		MIPI_CSI1_D3				
CSI2	MIPI_CSI3_D0	CAM_MCLK1	CAM_I2C1	CAMERA1_PDN	CAMERA1_RST	
						MIPI_CSI3_D1
						MIPI_CSI3_D2
						MIPI_CSI3_D3



# HDMI

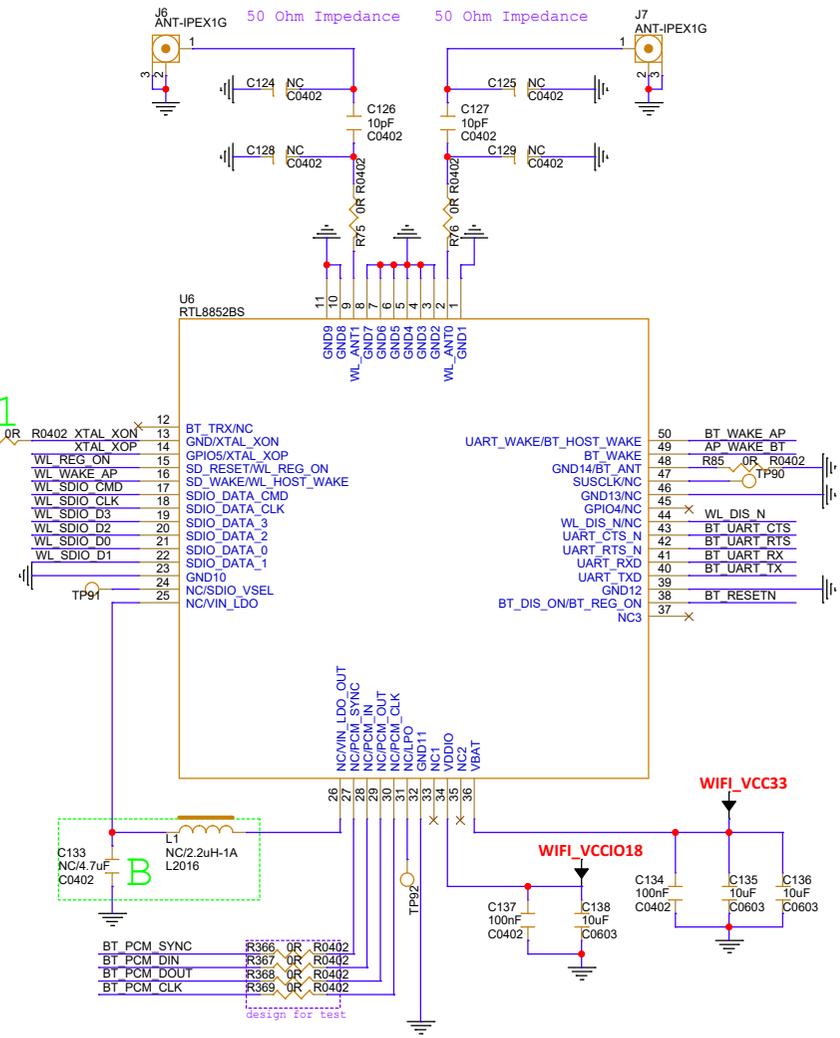
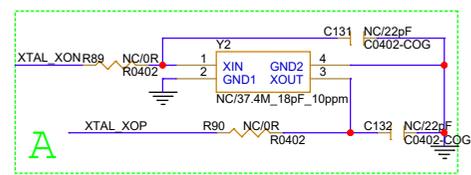
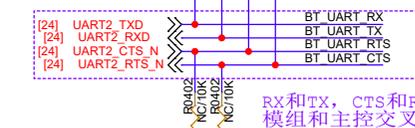
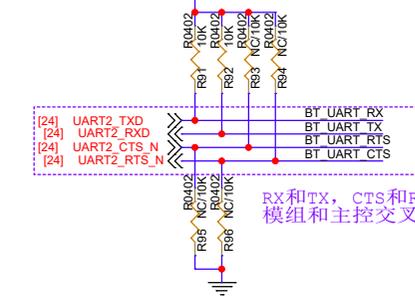
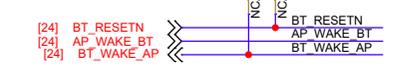
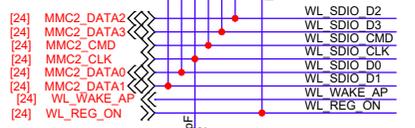
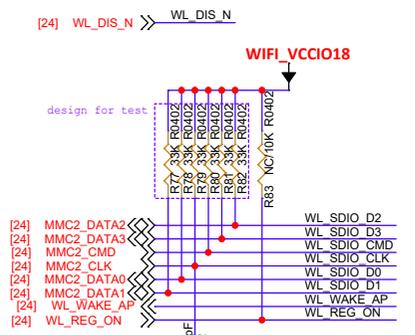


# MIPI-DSI



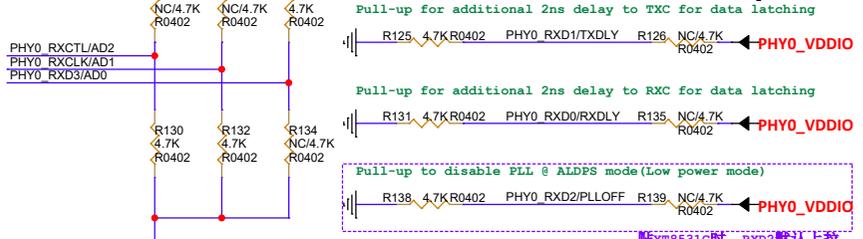
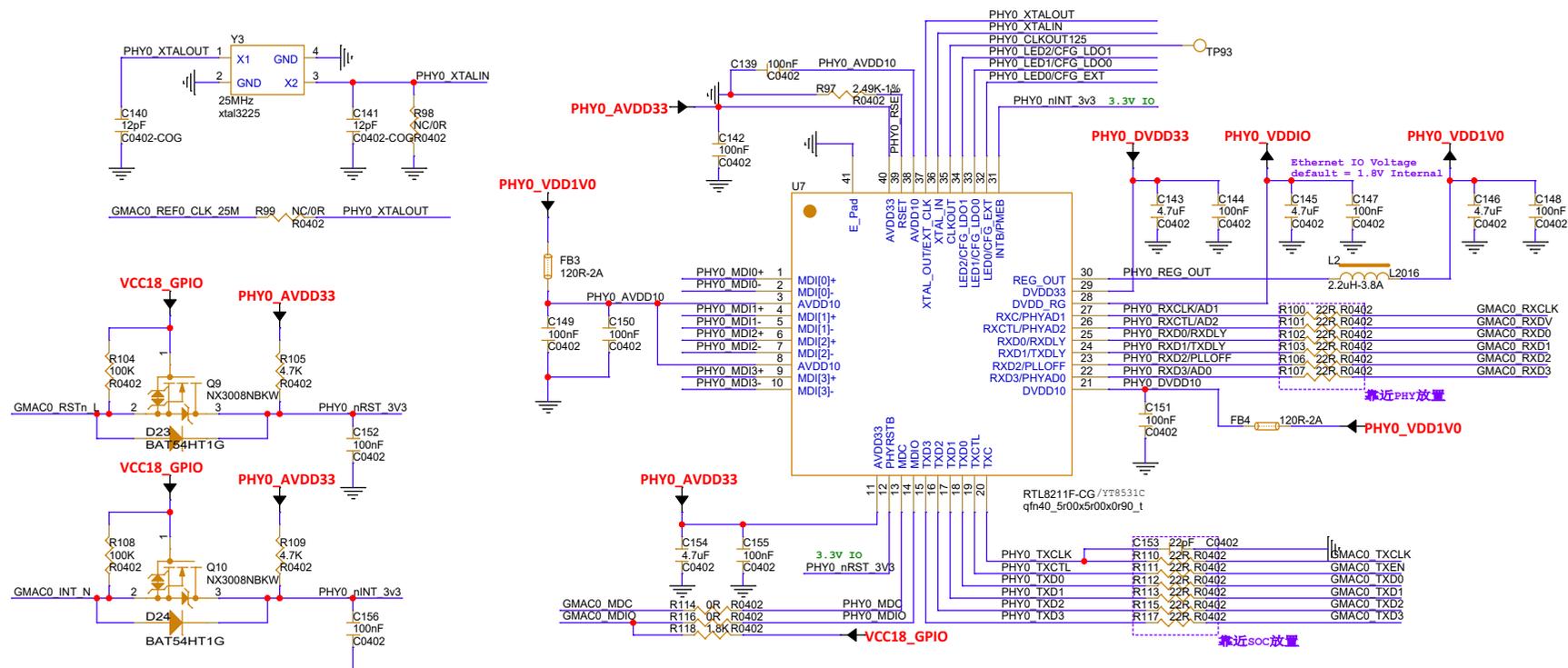
FH35C-31S-0.3SHW

# RTL8852BS



# GMAC Ethernet0

- GMACO\_TXD0 [24]
- GMACO\_TXD1 [24]
- GMACO\_TXD2 [24]
- GMACO\_TXD3 [24]
- GMACO\_TXEN [24]
- GMACO\_TXCLK [24]
- GMACO\_RXD0 [24]
- GMACO\_RXD1 [24]
- GMACO\_RXD2 [24]
- GMACO\_RXD3 [24]
- GMACO\_RXDV [24]
- GMACO\_RXCLK [24]
- GMACO\_REF0\_CLK\_25M [24]
- GMACO\_MDC [24]
- GMACO\_MDIO [24]
- GMACO\_RSTn\_L [24]
- GMACO\_INT\_N [24]



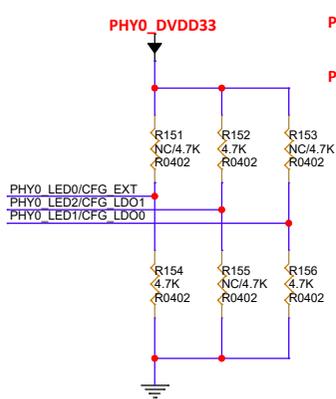
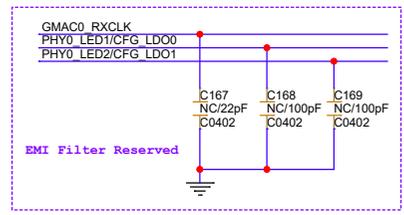
PHY Address	PHYAD[2:0]
1	001

Pull-up for additional 2ns delay to TXC for data latching

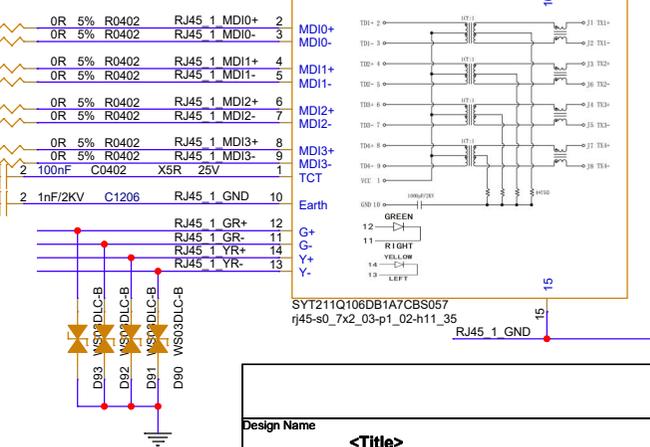
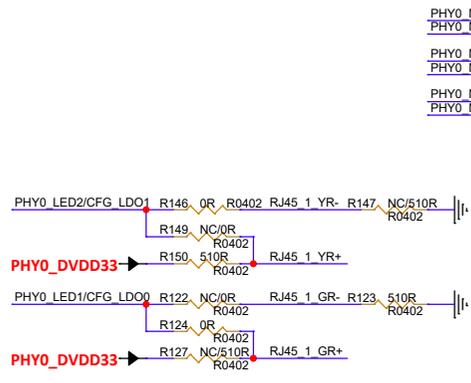
Pull-up for additional 2ns delay to RXC for data latching

Pull-up to disable PLL & ALDPS mode(Low power mode)

RTL8211F-CG, rxd2默认上拉

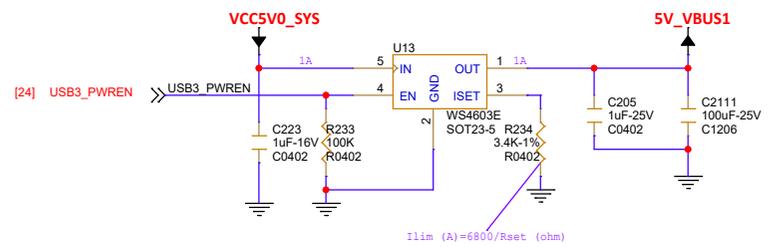
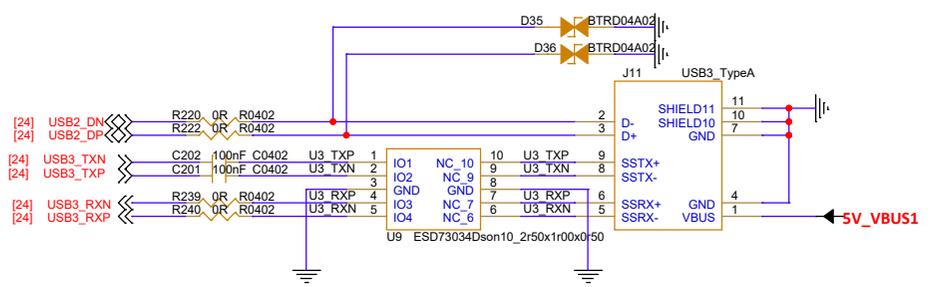


RGMI Power Source	CFG_EXT	CFG_LDO[1:0]
External 3.3V	1	00
External 2.5V	1	01
External 1.8V	1	10
External 1.5V	1	11
Internal 2.5V	0	01
Internal 1.8V(default)	0	10
Internal 1.5V	0	11

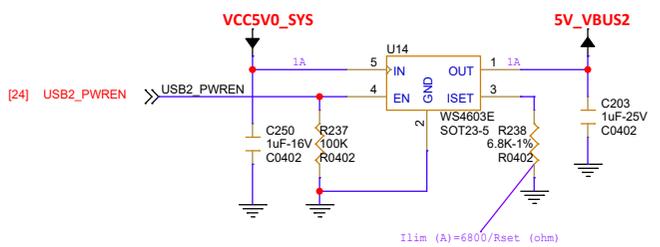
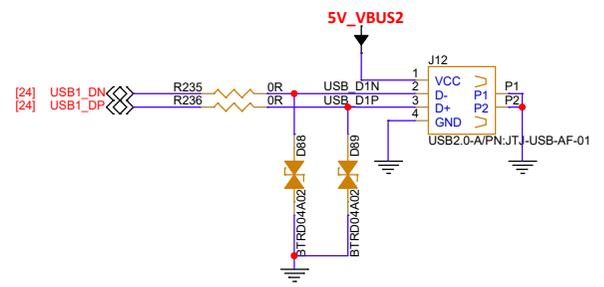




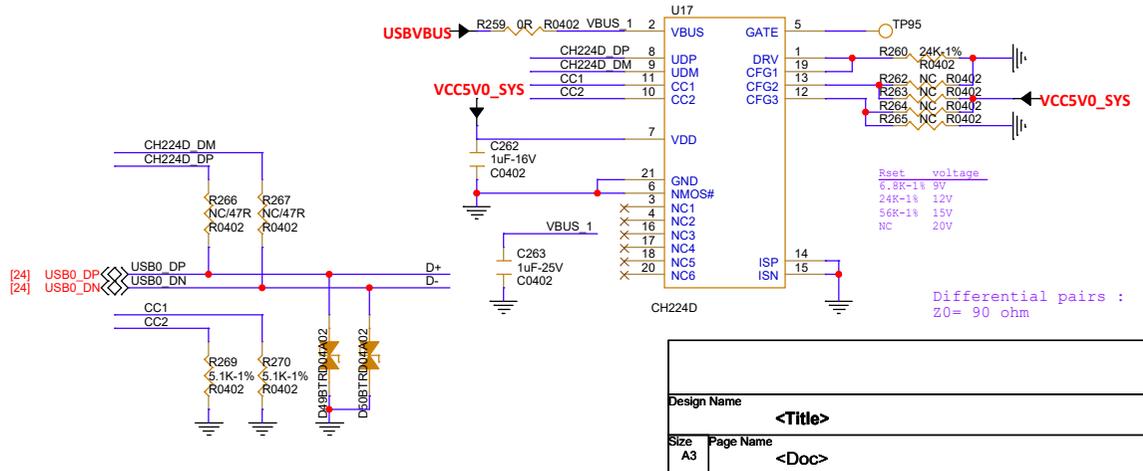
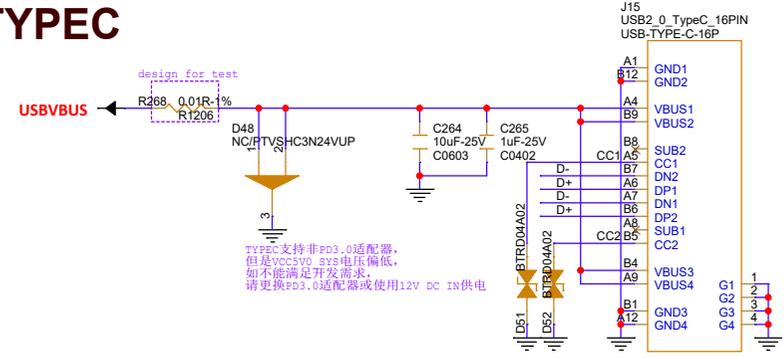
# USB3.0 TYPEA



# USB2.0 TYPEA-HOST



# USB2.0 TYPEC



design for test

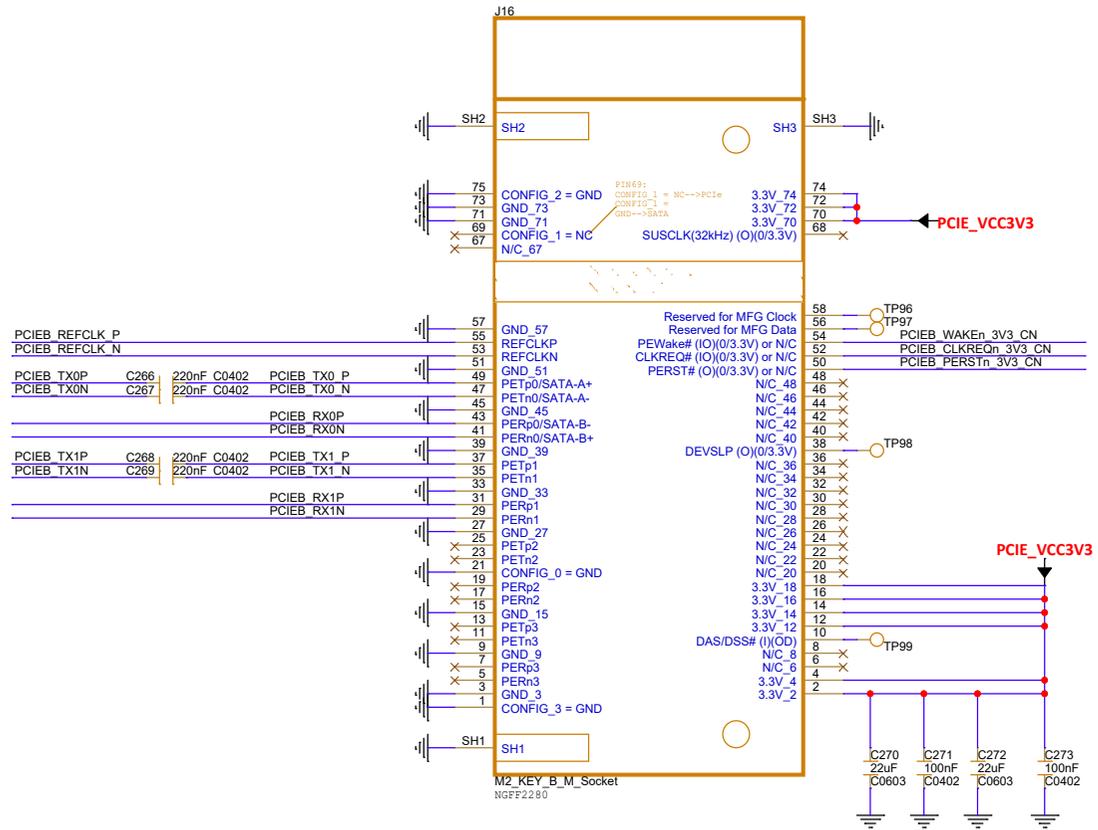
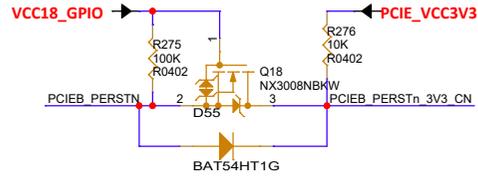
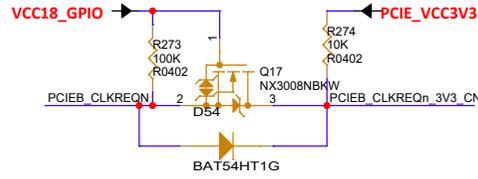
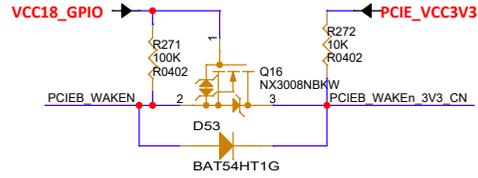
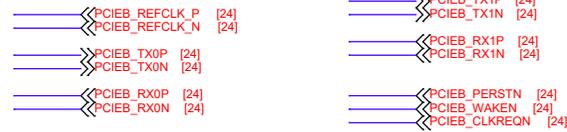
TVS支持非pd3.0适配器，但是VCC5V0\_SYS电压偏低，如不能满足开发需求，请更换PD3.0适配器或使用12V DC IN供电

Rset voltage

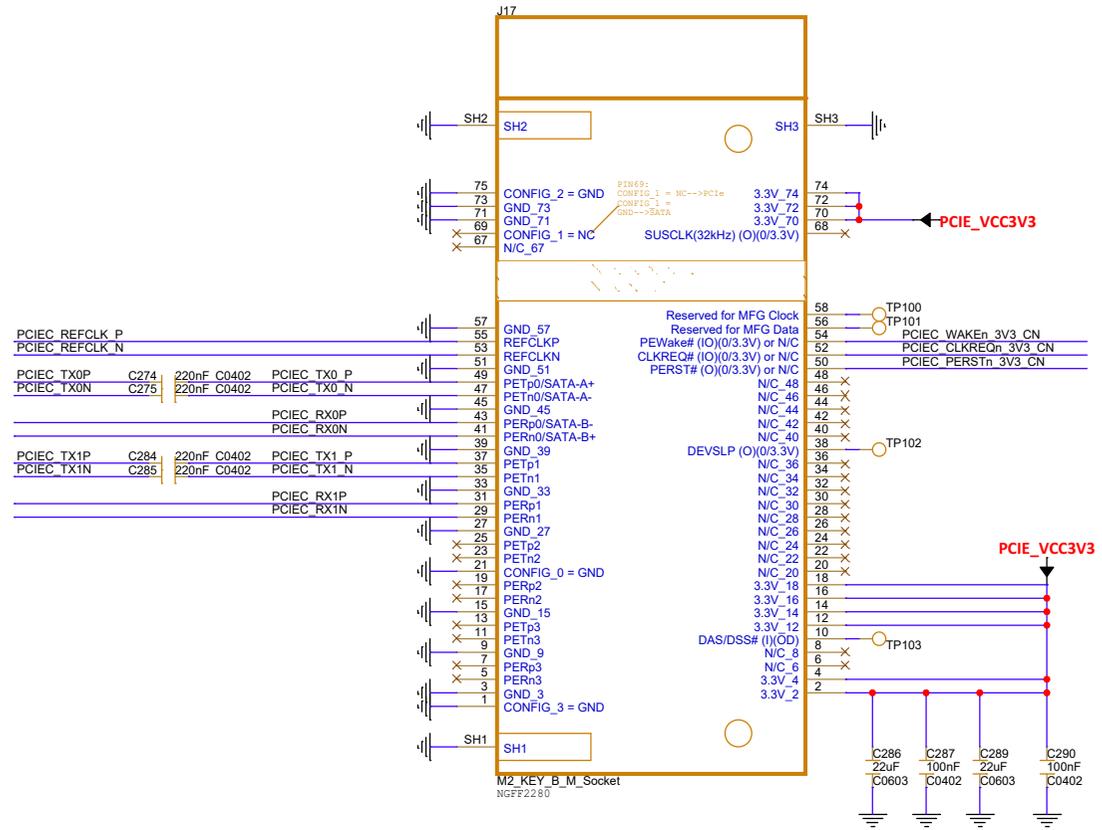
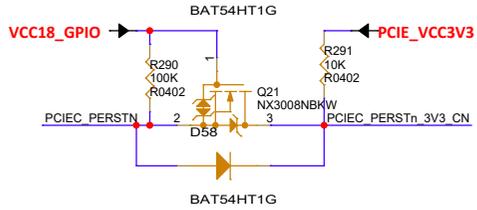
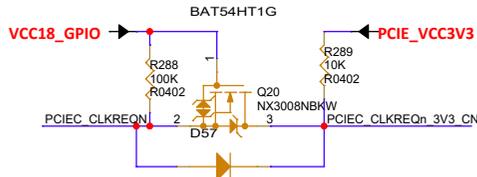
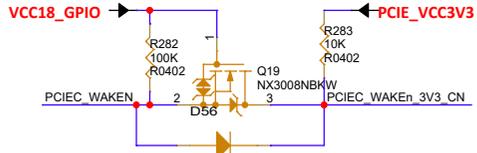
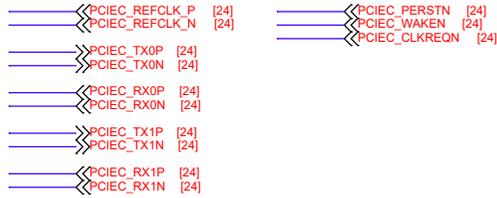
5.9K-1%	9V
24K-1%	12V
56K-1%	15V
NC	20V

Differential pairs : 20= 90 ohm

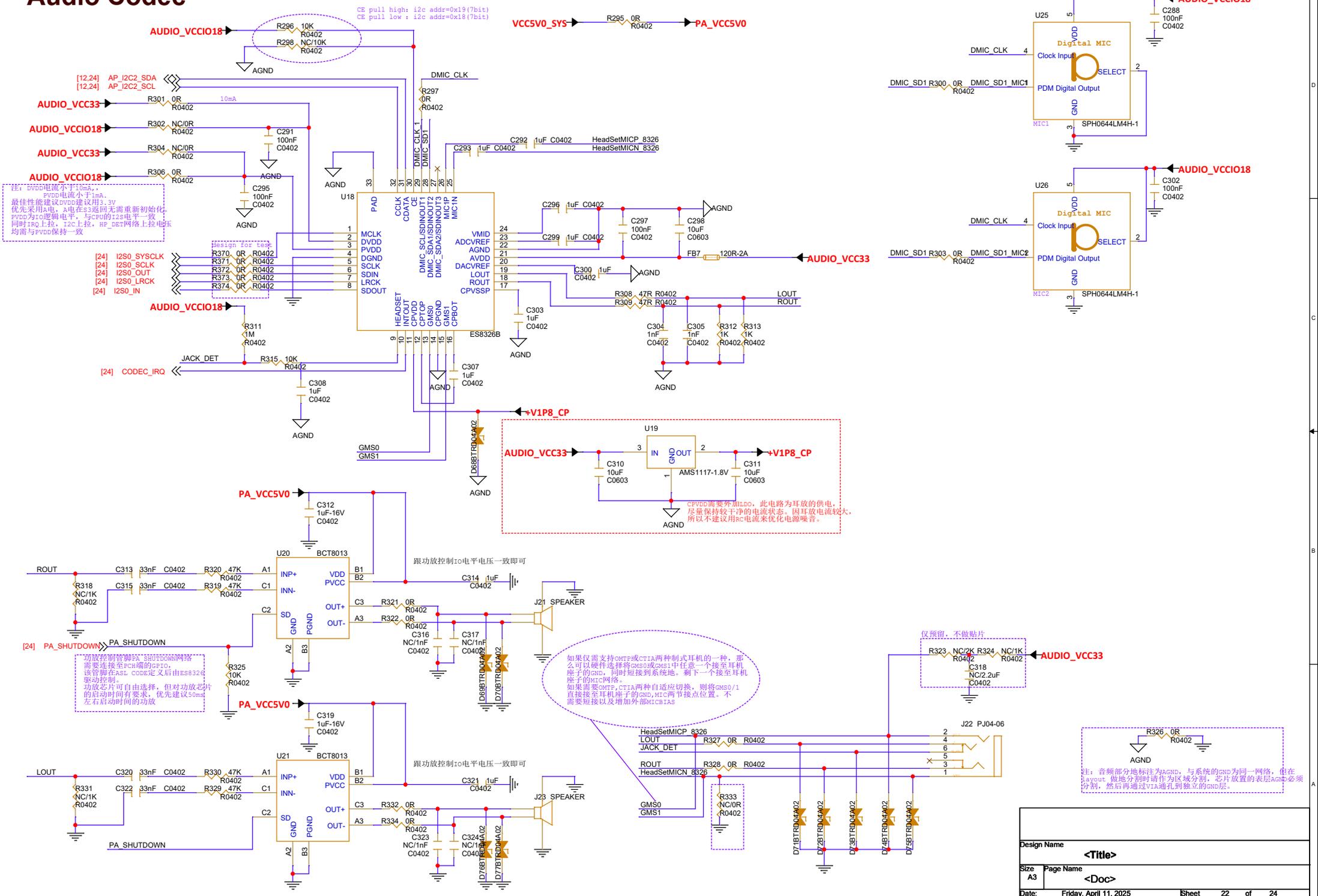
# PCIEB



# PCIEC



# Audio Codec



注: DVDD电流大于10mA, 且  
 DVDD电流小于1mA。  
 最佳性能建议DVDD建议用3.3V  
 优先采用A电, A电在s3返回无需重新初始化  
 DVDD为10毫安电平, 与CPU的125电平一致  
 同时I2C上拉, I2C上拉, mp\_pcs网络上拉电压  
 均需与PVDD保持一致

功放控制管脚PA\_SHUTDOWN网络  
 需要接至ES8326的GPIO。  
 该管脚在ASL CODE定义后由ES8326  
 驱动控制。  
 功放芯片可自由选择, 但对功放芯片  
 的启动时间有要求, 优先建议5ms  
 左右启动时间的功放

ES8326B需要外加LDO, 此电路为耳放的供电,  
 尽量保持较干净的电流状态, 因耳放电流较大,  
 所以不建议用RC电路来优化电源噪音。

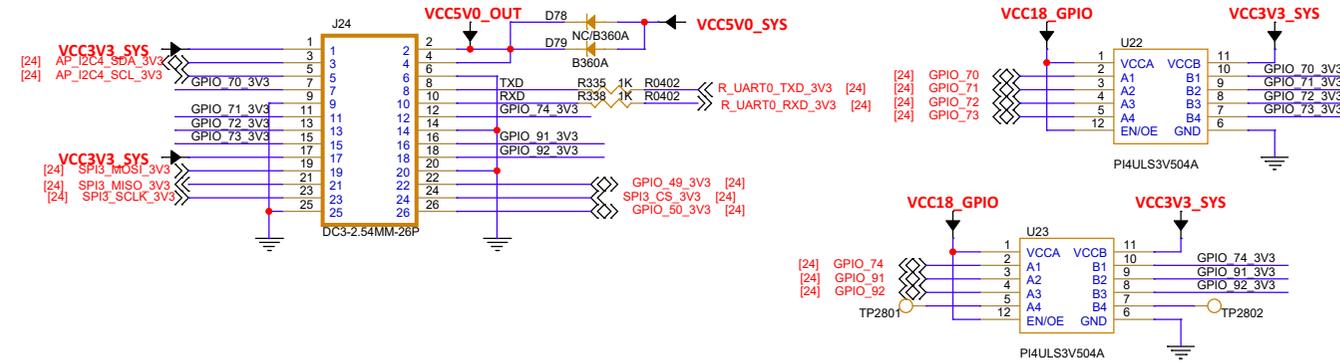
如果仅需支持OMTP或CTIA两种制式耳机的一种,  
 那么可以选择将GMS0或GMS1中任意一个接至耳机  
 座子的GND, 同时短接到系统地, 剩下一个接至耳机  
 座子的MIC网络。  
 如果需要OMTP, CTIA两种自适应切换, 则将GMS0/1  
 直接接至耳机座子的GND, MIC两节点位置, 不  
 需要短接以及增加外部MICBIAS

注: 音频部分地标注为AGND, 与系统的GND为同一网络, 但在  
 Layout做地分割时请作为区域分割, 芯片放置的表层AGND必须  
 分割, 然后再通过VIA通孔到独立的GND层。

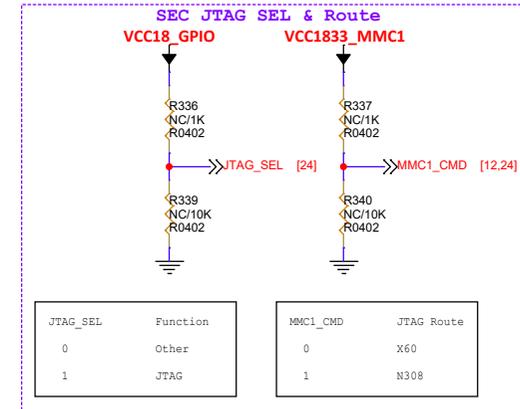
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Size A3	Page Name <Doc>
Date: Friday, April 11, 2025	Sheet 22 of 24

# 26 pin GPIO

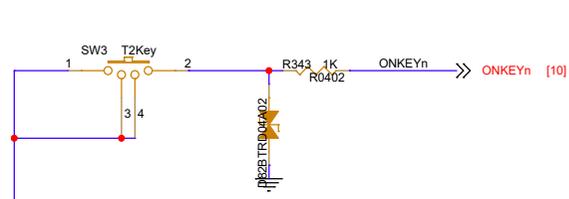
- 1. SPI\_LCD:GPIO\_70~74+GPIO\_91+GPIO\_92
- 2. PRI\_JTAG:GPIO\_70~73
- 3. N308 Debug:R\_UART0\_TXD\_3V3/R\_UART0\_RXD\_3V3



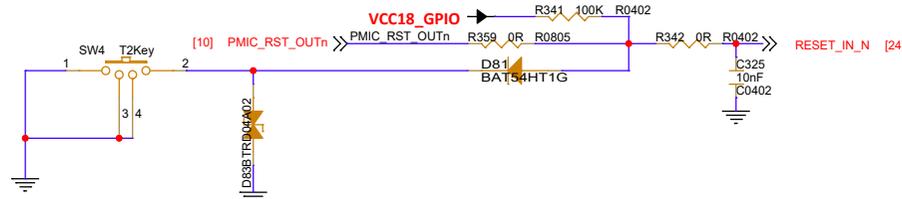
# JTAG SEL



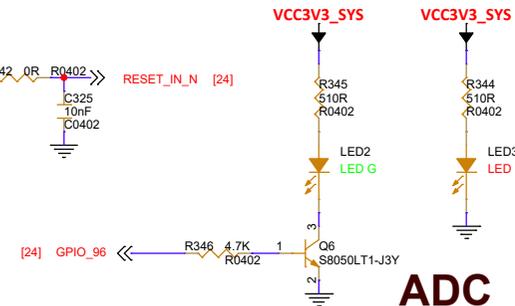
# PowerON



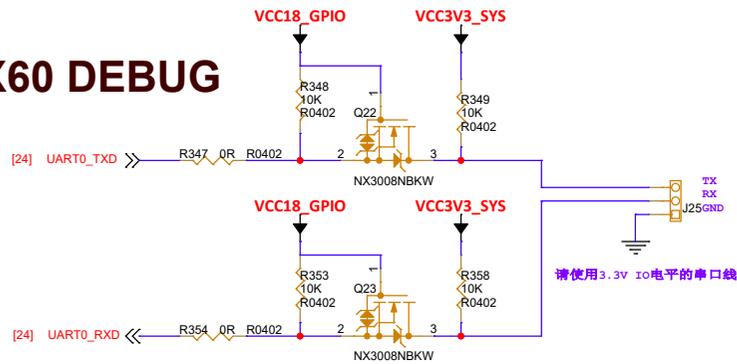
# REST



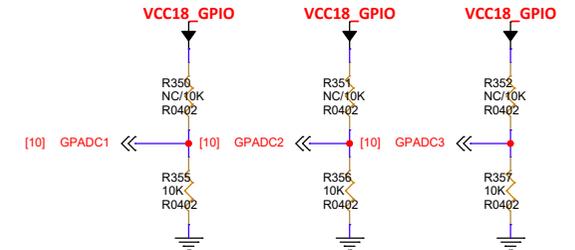
# LED



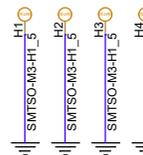
# X60 DEBUG



# ADC



# Mark Port



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